

NOV. 4. 1998 5:05PM

NO. 6076 P. 10/12

Jorney Docket No.: AMAT 524R1/T289

## DECLARATION

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am an original, first, and joint inventor of the subject matter which is described in U.S. Patent No. 5,571,571, issued November 5, 1996, and for which a broadening reissue patent is sought on the invention entitled **METHOD FOR FORMING A THIN FILM FOR A SEMICONDUCTOR DEVICE** the specification of which is attached hereto.

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56. I verily believe the original patent to be wholly or partly inoperative or invalid by reason of the patentees claiming less than they had the right to claim in the patent. One error upon which the reissue is based is that the patent does not claim a method of depositing a layer having a predetermined stress level by distributing a halogen source at a selected rate in a processing chamber.

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant.

## PRIORITY CLAIM (35 U.S.C. §119)

I hereby claim foreign priority benefits under Title 35, United States Code, §119, of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below, and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed.

## Prior Foreign Application(s)

Country	Application No.	Date of Filing	Priority Claimed Under 35 USC 119
Japan	3-145070	June 16, 1993	Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>

## PRIORITY CLAIM (35 U.S.C. §120)

I claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application No	Date of Filing	Status
08/184,331	January 19, 1994	Patented Pending <input checked="" type="checkbox"/> Abandoned

Full Name of Inventor 1:	Last Name:	First Name:	Middle Name or Initial:
Residence & Citizenship:	CITY	State/Foreign Country	Country of Citizenship
Post Office Address:	Post Office Address	CITY	State/Country Postal Code:
	4-3-6 Ryukakujinai, Sakae	Toba, Chiba 270-1505	Japan

NOV. 4. 1998 5:05PM

NO. 6076 P. 11/17

Jury Docket No.: AMAT 524R1/T289

Full Name of Inventor 2:	Last Name: MIZUNO	First Name: SHINSUKE	Middle Name or Initial:
Residence & Citizenship:	City Iaba	State/Foreign Country Chiba 270-1505	Country of Citizenship Japan
Post Office Address:	Post Office Address 3-7-11 Hyakakujidai, Sakae	City Iaba, Chiba 270-1505	State/Country Japan Postal Code:

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of Inventor 1 <i>Katsuyuki Musaka</i> KATSUYUKI MUSAKA Date 11/5/98	Signature of Inventor 2 <i>Shinsuke Mizuno</i> SHINSUKE MIZUNO Date Nov 4. 1998
--	--

"Express Mail" Label No. EL 140 093 034US  
Date of Deposit November 5, 1998

PATENT  
Attorney Docket No.: AM524R1/T289

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Address" service under 37 CFR 1.10 on the date indicated above and is addressed to:

Assistant Commissioner for Patents  
Washington, D.C. 20231

By: *Sandy K*

jc549 U.S. PTO  
09/18/551  
11/05/98

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

MUSAKA et al.

Patent No.: 5,571,571

Issue Date: November 5, 1996

For: **METHOD OF FORMING A THIN FILM  
FOR A SEMICONDUCTOR DEVICE**

Examiner: Unassigned

Art Unit: Unassigned

ASSENT OF ASSIGNEE AND  
OFFER TO SURRENDER PATENT

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

APPLIED MATERIALS, INC. assignee of U.S. Patent No. 5,571,571, assents to the above-identified application for reissue of said patent and hereby offers to surrender U.S. Patent No. 5,571,571.

Date 10/20/98

By *Barry Quan*

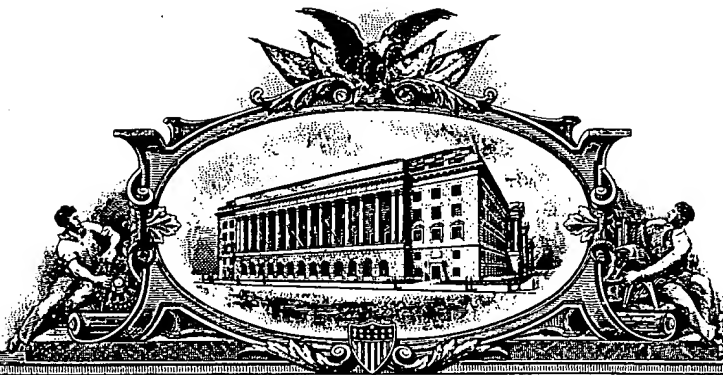
Name BARRY QUAN

Title ASSISTANT SECRETARY

TOWNSEND and TOWNSEND and CREW LLP  
Tel: (650) 326-2400  
Fax (650) 326-2422  
SWH:sgk

1c549 U.S. PTO  
09/187551

11/05/98



# THE UNITED STATES OF AMERICA

TO ALL TO WHOM THESE PRESENTS SHALL COME:

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office

October 06, 1998

THIS IS TO CERTIFY THAT ANNEXED IS A TRUE COPY FROM THE  
ASSIGNMENT RECORDS OF ALL DOCUMENTS FOUND OF RECORD  
AS OF THE DATE OF THIS CERTIFICATION FOR:

APPLICATION NUMBER: 08/259,584


FILING DATE: June 14, 1994

PATENT NUMBER: 5,571,571

ISSUE DATE: November 05, 1996



By Authority of the  
COMMISSIONER OF PATENTS AND TRADEMARKS

  
P. R. GRANT  
Certifying Officer

PATENT NUMBER: 5571571                      ISSUE DATE: 11/05/96  
SERIAL NUMBER: 08/259584                      FILING DATE: 06/14/94  
TITLE: METHOD OF FORMING A THIN FILM FOR A SEMICONDUCTOR DEVICE  
APPLICANT: MUSAKA, KATSUYUKI ; MIZUNO, SHINZUKE

REEL: 7161    FRAME: 0321    DATE RECORDED: 08/22/94    NUMBER OF PAGES: 003  
ASSIGNOR: MUSAKA, KATSUYUKI

                    EXC DATE: 08/10/94  
                    MIZUNO, SHINZUKE

                    EXC DATE: 07/25/94

ASSIGNEE: APPLIED MATERIALS, INC.  
          LEGAL AFFAIRS DEPARTMENT  
          3050 BOWERS AVENUE  
          M/S #0934  
          SANTA CLARA, CA 95054

BRIEF:  
ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

10/06/98 11:17

RETURN ADDRESS: ROBERT J. STERN  
                  APPLIED MATERIALS, INC.  
                  LEGAL AFFAIRS DEPT.  
                  3050 BOWERS AVE. M/S 0934  
                  SANTA CLARA, CA 95054

NO MORE INFORMATION FOR THIS PATENT NUMBER            10/06/98 11:17